		STUDY MODULE D	ES	CRIPTION FORM			
Name of the module/subject				Code			
Low-level programming				10103212610103			
Field of	study			Profile of study (general academic, practical)		Year /Semester	
Elec	trical Engineerin	g		(brak)		3/6	
Elective	path/specialty			Subject offered in:		Course (compulsory, elective)	
	Electrical S	ystems in Mechatronics	_	polish		obligatory	
Cycle of	study:		For	m of study (full-time,part-time)			
First-cycle studies				full-time			
No. of h	ours					No. of credits	
Lectur	e: 1 Classes	s: - Laboratory: 1		Project/seminars:	-	2	
Status of the course in the study program (Basic, major, other) (university-wide, from another field					ield)	-1-)	
F 1 <i>i</i>		(Drak)			(Dra		
Education	on areas and fields of sci	ence and art				and %)	
techr	nical sciences					2 100%	
Resp	onsible for subi	ect / lecturer:				1	
drin	z Diotr Suiko						
ema	il: piotr.sujka@put.po	znan.pl					
tel.	+48 61 665 2662	•					
Elek	tryczny						
UI. F	10trowo 3A 60-965 PC	Dznan					
Prere	quisites in term	s of knowledge, skills an	d s	ocial competencies:			
1	Knowledge	Basic knowledge of mathematic	s, electronics and computer science.				
2	Skills	Skills of effective self-education in a field related to the chosen field of study, the ability to make the right decisions when designing algorithms and writing the program, the ability to use the Windows operating system on a general level.					
3	Social competencies	A student is aware of the widening their competence, has a willingness to work in a team, the ability to comply with the rules existing in the classroom lecture and laboratory.					
Assu	mptions and obj	ectives of the course:					
1. Prov system	ride students with basi	c knowledge about the operation	of pi	ocessors (construction, ins	struc	tion set) and microprocessor	
2. Provide students with a basic knowledge of binary arithmetic and logic.							
3. Gair	ing the students' abilit	ty to write simple programs in ass	semb	ler language.			
4. Deve	eloping students' team	work skills.					
	Study outco	mes and reference to the	ed	ucational results for	a f	ield of study	
Know	/ledge:						
1. A stu industr	udent has a basic kno ies - [K_W07+++]	wledge of the structure and opera	ation	of microprocessor systems	and	d their application in selected	
2. A stu	udent has an elementa	ary knowledge of information tech	nolo	gy, used in electrical engine	eeri	ng, the architecture of	
software and microprocessor systems [K_W11+]							
Skills:							
1. A student can formulate an algorithm and knows how to write a program in assembler [K_U04++]							
2. A students can use a property chosen development environments, CPU emulators and programmers [K_U13+]							
1. Student con think and act in an entroproportial manner in the field of 22electrical environments. IV, K04++1							
Assessment methods of study outcomes							

Lecture:							
- Assess the knowledge and skills demonstrated in the form of a written test;							
- Continuous assessment for each course (rewarding activity and quality perception).							
laboratory:							
- Current assessment on each course based on the severity of the problem solution							
- Assessment of reports.							
Get extra points for the activity in the classroom, and especially for:							
- Propose to discuss further aspects of the subject;							
 Comments related to the improvement of teaching materials; 							
- Developed esthetic accuracy reports - in the self-study.							
Course description							
Construction of the CPU: ALU, registers, ports. ROM and RAM memory.							
Binary arithmetic: Binary code, BCD code, U2 code.							
Assembler - syntax.							
CPU instruction set: arithmetic and logical, branching and jumping, data transfer, bit operations.							
Interrupts.							
Basic bibliography:							
1. Daca W.: Mikrokontrolery od układów 8-bitowych do 32-bitowych, Wyd. NIKOM, Warszawa, kwiecień 2000.							
2. Michalski J. A.: Mikroklocki. Mikroprocesory dla początkujących, Wyd. BTC, Warszawa 2007.							
3. Doliński J.: Mikrokontrolery AVR w praktyce, Wyd. BTC, Warszawa 2003.							
Additional bibliography:							
1. Doliński J.: Mikrokontrolerv AVR - niezbednik programisty. Wvd. BTC. Legionowo 2009.							
2. Pasierbiński J., Zbysiński P.: Układy programowalne w praktyce, Wyd. WKŁ, Warszawa 2002.							
Result of average student's workload							
Activity		Time (working					
Activity		hours)					
1. participation in lectures		15					
2. participation in laboratory	15						
3. participation in consultations	5						
4. preparation for laboratory	6						
5. making reports	10						
6. preparation for the pass of lectures	6						
7. presence at the lecture exam	3						
Student's workload							
Source of workload	hours	ECTS					
	60	2					
	00	۷					

Contact hours

Practical activities

38

32

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